



*IFW*

Docket No.: 67161-112

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Yasunobu NAKASE	:	Confirmation Number: 1468
Application No.: 10/677,012	:	Group Art Unit: 2818
Filed: October 02, 2003	:	Examiner: Hoai V. Ho

For: SEMICONDUCTOR MEMORY DEVICE HAVING POTENTIAL AMPLITUDE OF  
GLOBAL BIT LINE PAIR RESTRICTED TO PARTIAL SWING

**AMENDMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The following amendment and remarks are submitted in response to the Office Action  
dated October 29, 2004.

Amendments to the claims appear in the current listing of claims that begins on page 2 of  
this paper.

Remarks begin on page 7 of this paper.